

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor

: Michiaki Maruoka

Serial No

10/024,103

Filed

December 17, 2001

Title

Semiconductor Device Having Bonding...

Group Art Unit

December 20, 2002

Attn: Customer Corrections Division Assistant Commissioner for Patents

Washington, D.C. 20231

REQUEST FOR CORRECTED FILING RECEIPT

SIR:

We received the filing receipt on the above-referenced case, copy enclosed,

wherein the FOREIGN APPLICATION was omitted. Please add: JAPANESE PATENT

APPLICATION NO. 2000-394040 FILED: DECEMBER 26, 2000.

We sent a first request to correct the filing receipt on March 29, 2002 no response has been received from the first request.

Any fee, due as a result of this paper may be charged on Deposit Account No. 50-1290.

respectating supplified

Michael Markowitz

Reg. No. 30,659

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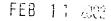
Docket No.: NECB 19.265 (100806-00068)

I HEREBY CERTIES TENS CORRESPONDENCE IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO: COMMISSIONER OF PATENTS AND TRADEMARKS, WASHINGTON, D.C. 20231, ON THE DATE INDICATED BELOW.

By Sharren Alensis

DATE Desember 20, 2002

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FIL FEE REC'D ATTY.DOCKET.NO DRAWINGS TOT CLAIMS IND CLAIMS APPLICATION NUMBER GRP ART UNIT FILING DATE **NECB** 2 740 11 20 10/024,103 12/17/2001

19.265

HELFGOTT & KARAS P.C.

60th Floor **Empire State Building** New York, NY 10118-0110 FILING RECEIPT

OC000000007385494

Date Mailed: 01/29/2002

CONFIRMATION NO. 3507

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE. NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Michiaki Maruoka, Tokyo, JAPAN;

Domestic Priority data as claimed by applicant

Please insert: opanese Patent Appln. No. 2000-894048 Filed: December 26,2000 Foreign Applications

If Required, Foreign Filing License Granted 01/29/2002

Projected Publication Date: 06/19/2003

N n-Publication Request: No

Early Publication Request: No

Title

Semiconductor device having bonding pad electrode of multi-layer structure

Preliminary Class

438



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